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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Romain DESPLATS et al.

Serial No. 09/831,525

Filed May 10, 2001



Group 2184

METHOD AND INSTALLATION FOR
FAST LOCATION OF A FAULT IN AN
INTEGRATED CIRCUITSECOND REQUEST FOR CORRECTED FILING RECEIPT

Assistant Commissioner for Patents

Washington, D.C. 20231

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Technology Center 2100

Sir:

We enclose herewith copies of the Request for Corrected Filing Receipt, filed on November 26, 2001, as evidenced by the accompanying postcard receipt. We ask that the Official Corrected Filing Receipt be recorded and returned to us in due course.

Respectfully submitted,

YOUNG & THOMPSON

By

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September 19, 2002

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November 26, 2001

Box PATENT APPLICATION
Commissioner of Patents
Washington, D.C. 20231

Dear Sir:

REQUEST FOR CORRECTED FILING RECEIPT

ATTENTION: Office of Initial Patent
Examination's Customer
Service Center

Re: Serial No. 09/617,499

Receipt is acknowledged of the official filing certificate for Serial No. 09/831,525, filed May 10, 2001, in the names of Romain DESPLATS et al., in Group 2184.

It is requested that a new certificate be issued on which the total number of claims is correctly given as "35".

Respectfully submitted,

YOUNG & THOMPSON

By

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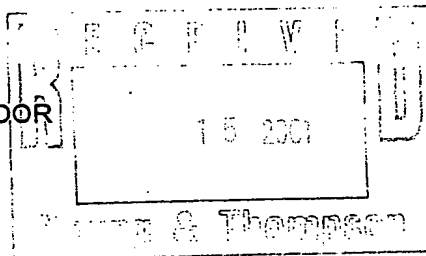


UNITED STATES PATENT AND TRADEMARK OFFICE

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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/831,525	05/10/2001	2184	1130	BE-9197	13	35	1

000466
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CONFIRMATION NO. 5660
FILING RECEIPT
OC000000006180094

Date Mailed: 06/14/2001

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Domestic Priority data as claimed by applicant

THIS APPLICATION IS A 371 OF PCT/FR99/01212 05/21/1999

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Foreign Applications

FRANCE 98/14277 11/13/1998

Projected Publication Date: N/A

Non-Publication Request: No

Early Publication Request: No

Title

Method and installation for fast fault localization in an integrated circuit

Preliminary Class

714

Data entry by : ORDENEZ, MARTA

Team : OIPE

Date: 06/14/2001



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Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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BY SUCH STAMP.

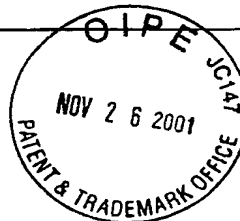
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S.N. 09/617,525 Group BOX PATENT APPLICATION

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